

		ISSUE CLASSIFICATION
	Class : Subclass	

FILED UNDER 35 U.S.C. 371

**PATENT NUMBER**

## **U.S. UTILITY Patent Application**

 <b>O.I.P.E.</b>	<b>PATENT DATE</b>
SCANNED  Q.A. 	

APPLICATION NO. 09/720860	CONT/PRIOR D F	CLASS 257	SUBCLASS 734	ART UNIT 2811 2817	EXAMINER THAI
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Nobuaki Hashimoto

**E** Semiconductor device, method of manufacture thereof, circuit board, and electronic device

PTO-2040  
12/89

## **ISSUING CLASSIFICATION**

TERMINAL DISCLAIMER	DRAWINGS			CLAIMS ALLOWED	
	Sheets Drwg.	Figs. Drwg.	Print Fig.	Total Claims	Print Claim for O.G.
<input type="checkbox"/> The term of this patent subsequent to _____ (date) has been disclaimed.				NOTICE OF ALLOWANCE MAILED	
	(Assistant Examiner)				
<input type="checkbox"/> The term of this patent shall not extend beyond the expiration date of U.S Patent No. _____				ISSUE FEE	
	(Primary Examiner)				
<input type="checkbox"/> The terminal _____ months of this patent have been disclaimed.				ISSUE BATCH NUMBER	
	(Legal Instruments Examiner)				

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